


FORM PTO-1449	Atty. Docket No.: L13.12-0157/01-191	Appl. No.: 09/879,416
 U.S. DEPT. OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Ian M. Flanagan et al.	
	Filing Date	Group Art:
	June 12, 2001	

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						

RECEIVED

JAN 23 2002

Technology Center 2600

## FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AL						
AM						
AN						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	F.M. Gardner, "Charge-Pump Phase-Lock Loops", IEEE Transactions on Communications, Vol COM-28, No. 11, November 1980, pp. 1849-58.
AP	W.F. Egan, "Phase Lock Basics", Wiley Interscience Publications, New York, 1998, pp. 90-91.
AQ	P.V. Brennan, "Phase Locked Loops: Principles and Practice", McGraw-Hill, New York, 1996, p. 35.
AR	D.H. Wolaver, "Phase-Locked Loop Circuit Design", PTR Prentice Hall, New Jersey, 1991, pp. ix-x.

EXAMINER:

DATE CONSIDERED: 8/12/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.